# Subject Index to Volume 19


32-Bit 19 (1987) 59
21-Bit Microprocessor 19 (1987) 203

Address Allocation 19 (1987) 291
Advanced Microprocessors 19 (1987) 277
Application Oriented Language 19 (1987) 1
Arbitration 19 (1987) 5
Architectures for Logical Programming 19 (1987) 385

Balanced Load Queueing Model 19 (1987) 401
BCD Number System 19 (1987) 119
BCD Packed Decimal Numbers 19 (1987) 119
Binary Arithmetic 19 (1987) 119
Binary Number System 19 (1987) 119
Binary to Decimal Conversion 19 (1987) 119
Bus Analyzer Processor 19 (1987) 401

Cars 19 (1987) 1
Clusters 19 (1987) 129
Code Optimisation 19 (1987) 367
Colour Picture Converter 19 (1987) 27
Composite Images 19 (1987) 19
Computer Aided Instruction 19 (1987) 417
Computer Evaluation 19 (1987) 49
Computer Networks 19 (1987) 143
Computer Organization 19 (1987) 417
Controlflow – Dataflow Evaluation 19 (1987) 385
Cost Efficiency 19 (1987) 1

Dataflow 19 (1987) 385
DC-Motor 19 (1987) 227
Decimal to Binary Conversion 19 (1987) 119
Design Rule Checking 19 (1987) 85
Digital Hardware Synthesis 19 (1987) 305
Digital Image Processing 19 (1987) 19, 27
Distributed Clock 19 (1987) 153
Distributed Computing Systems 19 (1987) 143
Distributed Program 19 (1987) 219
Distributed System 19 (1987) 49
Distributed Logic 19 (1987) 407

Edge Correction 19 (1987) 19
Edge Detection 19 (1987) 19
Electric Utility 19 (1987) 101
Elimination of Instruction Address Transfer 19 (1987) 407
Embedded Control Systems 19 (1987) 385

Fault Tolerance 19 (1987) 335
Financial Modelling 19 (1987) 101
Firmware Development System 19 (1987) 291
Flexibility 19 (1987) 1
Frame Store 19 (1987) 27
Function Tree 19 (1987) 27

Gate Level Simulation 19 (1987) 417

Hamiltonian Ring Structure 19 (1987) 219
Harvard Architecture 19 (1987) 407
Hierarchical Program Systems 19 (1987) 27
High Level Language 19 (1987) 277
High Level Language Support 19 (1987) 203

IBM PC-XT 19 (1987) 227
Image Processing 19 (1987) 5
Image Segmentation 19 (1987) 19
Instruction Issue 19 (1987) 407
Instruction Preinterpretation 19 (1987) 407
Intel 80386 19 (1987) 59
Interactive Cross Compiler 19 (1987) 367
Intermediate Code 19 (1987) 1
Interpreter 19 (1987) 1
Interprocess Communication 19 (1987) 319
Interrupts 19 (1987) 233

Laser Range Finder 19 (1987) 193
Local Area Network 19 (1987) 1
Local Instruction Buffers 19 (1987) 407
Local Selective TAS Flip Flop 19 (1987) 319
Logical Machine 19 (1987) 375
Logical Tokens' Relations 19 (1987) 385

Machine Description Language 19 (1987) 291
Machine-Independent 19 (1987) 291
Memory Coupled Systems 19 (1987) 129
Memory Interference 19 (1987) 401
Message Communication 19 (1987) 219
Message Coupled Systems 19 (1987) 129
Microarchitecture 19 (1987) 291, 305
Microcomputers 19 (1987) 101, 417
Microcomputers Operating Systems 19 (1987) 375
Microinstruction 19 (1987) 291
Microprocessor 19 (1987) 59, 193, 327
Microprogram 19 (1987) 233, 291
Microprogram Compaction 19 (1987) 305
Microprogram Generating System 19 (1987) 305
Microsequencer 19 (1987) 305

Migration of Functions 19 (1987) 407
Mini/Microcomputer Applications 19 (1987) 143
Minimum-Variance Control 19 (1987) 227
Monitors 19 (1987) 129
Multimaster Serial Bus 19 (1987) 153
Multiplex Wiring 19 (1987) 1
Multiprocessing System 19 (1987) 179
Multiprocessor 19 (1987) 5, 85, 153
Multiprocessor Monitor 19 (1987) 319
Multiprocessor Systems 19 (1987) 335

Object Oriented Languages 19 (1987) 129
Off-Line Testing 19 (1987) 37
Operating System 19 (1987) 203, 233
Operating System Support Features 19 (1987) 277
Optimal Number of Processor Elements 19 (1987) 401
OR-ed Dataflow 19 (1987) 385

Packed Decimal Arithmetic 19 (1987) 119
Parallel Algorithms 19 (1987) 179
Parallel Debugging Tool 19 (1987) 401
Parallel Processing 19 (1987) 85, 129
Pattern Recognition 19 (1987) 327
Performance 19 (1987) 233
Performance Analysis 19 (1987) 5
Performance Evaluation 19 (1987) 179
Peripheral 19 (1987) 193
Petri Nets 19 (1987) 335
Preprocessor 19 (1987) 129
Problems of Developing Countries 19 (1987) 101
Process Description Table 19 (1987) 129
Process Level Dataflow 19 (1987) 385
Process Synchronization 19 (1987) 319
Processor Communications 19 (1987) 367
Program Storage Modules 19 (1987) 407

Queueing Applications 19 (1987) 143

Random Testing of Microprocessors 19 (1987) 37
Real Time Control Systems 19 (1987) 385
Real Time Performance 19 (1987) 401
Real-Time 19 (1987) 233
Register Transfer Level Simulation 19 (1987) 417
Register Transfer Operation 19 (1987) 305
Relational Database II and III 19 (1987) 101
Renewal Theory 19 (1987) 233
Resource Sharing 19 (1987) 143

Self-Tuning 19 (1987) 227
Serial Bus Protocol 19 (1987) 153
Shared Memory Analysis 19 (1987) 143
Signal Processing 19 (1987) 327
Single Chip Microcomputers 19 (1987) 1
Stack Languages 19 (1987) 367
Structured Design 19 (1987) 153
Supervisory and Cluster Kernels 19 (1987) 129
Synchronous Bus 19 (1987) 5
System Diagnosis 19 (1987) 327
System Modelling and Analysis 19 (1987) 143

Task Allocation 19 (1987) 335
Terminal Performance 19 (1987) 143
Test Length 19 (1987) 37
Testbed 19 (1987) 49
Tightly Coupled Computers 19 (1987) 129
Transputer 19 (1987) 367
Tree-like Multiprocessors 19 (1987) 129
True-Colour Images 19 (1987) 19
Two-Way Instruction Prefetching 19 (1987) 407

UTC Common Bus Multiprocessor 19 (1987) 319
UTC Common Bus System 19 (1987) 401

Virtual Machine 19 (1987) 375
VLSI 19 (1987) 85

North-Holland
Microprocessing and Microprogramming 19 (1987) 461–462



# Author Index to Volume 19

Abdelhay, S.A. and Azharul-haque, M., DC-Motor Control Using a Minimum-Variance Self-Tuner 19 (1987) 227
Ahmad, M., Implementable Decimal Arithmetic Algorithms for Micro/Minicomputers 19 (1987) 119
Al-Rabeh, A.H., *see* Gough, N.E. 19 (1987) 167
Allo, G., and Staszny, G., Two Algorithms for Image Segmentation 19 (1987) 19
Allo, G., Staszny, G. and Fero, L., Colour Picture Converter for Image Processing 19 (1987) 27
Andima, H.S. and Arungu-Olende, S., The Use of Microcomputers for Financial Modeling of a Public Utility System in a Developing Country 19 (1987) 101
Arora, R.K., Rana, S.P. and Gupta, M.N., Ring Based Termination Detection Algorithm for Distributed Computations 19 (1987) 219
Arungu-Olende, S., *see* Andima, H.S. 19 (1987) 101
Azharul-haque, M., *see* Abdelhay, S.A. 19 (1987) 227

Bae, P., *see* Bina, M. 19 (1987) 327
Balakrishnan, M., Bhatt, P.C.P. and Madan, B.B., An Efficient Retargetable Microprogram Generating System 19 (1987) 305
Beer, M.D., *see* Tsai, S.R. 19 (1987) 375
Belomorski, P.G., Pseudorandom Self-Testing of Microprocessors 19 (1987) 37
Bhatt, P.C.P., *see* Balakrishnan, M. 19 (1987) 305
Bina, M., Bae, P. and Gaillard, P., Pattern Recognition Machine: Its Conception and Creation 19 (1987) 327

Civera, P.L., *see* Del Corso, D. 19 (1987) 153

Del Corso, D., Civera, P.L., Reyneri, L. and Sansoe, C., An Integrated Controller for Modified Inter-Integrated Circuit Protocol 19 (1987) 153

El-Sakkary, A.K., *see* Gough, N.E. 19 (1987) 167

Fero, L., *see* Allo, G. 19 (1987) 27
Fontenier, G., *see* Hou, K.M. 19 (1987) 319, 401

Gaillard, P., *see* Bina, M. 19 (1987) 327
Ghosal, D. and Patnaik, L.M., SHAMP: An Experimental Shared Memory Multimicroprocessor System for Performance Evaluation of Parallel Algorithms 19 (1987) 179
Giordano, A., Giunchiglia, F., Maresca, M. and Vernazza, T., Evaluation of a Multiprocessor Structure for Image Processing 19 (1987) 5
Giunchiglia, F., *see* Giordano, A. 19 (1987) 5
Gough, N.E., Al-Rabeh, A.H., Halang, W.A., El-Sakkary, A.K. and Jamin, N.C., U.P.M. Centre of Research in Computer Control 19 (1987) 167
Gregoretti, F. and Segall, Z., Analysis and Evaluation of Parallel Rectangle Intersection for VLSI Design Rule Checking 19 (1987) 85
Gupta, M.N., *see* Arora, R.K. 19 (1987) 219

Halang, W.A., A Distributed Logic Program Instruction Prefetching Scheme 19 (1987) 407
Halang, W.A., *see* Gough, N.E. 19 (1987) 167
Hou, K.M. and Fontenier, G., U.T.C. Multiprocessor Hardware Structure and Software Approach to Design its Monitor 19 (1987) 319
Hou, K.M., Fontenier, G. and Senadji, M., A Simple Bus Analyzer: Parallel Debugging Tool and Real Time Performance Measure Instrument and its Application 19 (1987) 401

Isoda, S., Machine-Independent Microprogram Address Allocation through Hierarchical Structuring 19 (1987) 291

Jamin, N.C., *see* Gough, N.E. 19 (1987) 167
Johansson, L.T., A Computer Architecture Suited to Multiplex Wiring Systems in Cars 19 (1987) 1

Lent, B., Data Flow Driven Computer for Embedded Control Systems 19 (1987) 385
Lombardi, F., *see* Rathael, S. 19 (1987) 49

Madan, B.B., *see* Balakrishnan, M. 19 (1987) 305
Maresca, M., *see* Giordano, A. 19 (1987) 5
Mayer-Lindenberg, F., FIFTH on the Transputer 19 (1987) 367

Mok, K.Y., *see* Ng, K.W. 19 (1987) 203
Monchaud, S., Palotasi, A. and Prat, R., Microprocessor Tasks for a Laser Range Finding Peripheral 19 (1987) 193

Ng, K.W. and Mok, K.Y., The High Level Language and Operating System Support Features of Advanced Microprocessors – Part I: High Level Language Support Features 19 (1987) 203
Ng, K.W., The High Level Language and Operating System Support Features of Advanced Microprocessors – Part II: Operating System Support Features 19 (1987) 277

Palotasi, A., *see* Monchaud, S. 19 (1987) 193
Papazoglou, M. and Pintelas, P., A Hierarchical Multimicroprocessor System for Object Oriented Languages 19 (1987) 129
Patnaik, L.M., *see* Ghosal, D. 19 (1987) 179
Pintelas, P., *see* Papazoglou, M. 19 (1987) 129
Piuri, V. and Tourouta, E., About Fault-Tolerant Allocation of Tasks in Multiprocessor Architectures and System Dimensioning 19 (1987) 335
Prat, R., *see* Monchaud, S. 19 (1987) 193

Rana, S.P., *see* Arora, R.K. 19 (1987) 219
Rathael, S. and Lombardi, F., A Software Testbed for the Design and Evaluation of Distributed Computer Systems 19 (1987) 49
Reddi, A.V., Shared Mini/Microcomputer Memory Performance at Remote Computer Network Nodes in Large Scale Distributed Computing Systems 19 (1987) 143
Reyneri, L., *see* Del Corso, D. 19 (1987) 153

Sansoe, C., *see* Del Corso, D. 19 (1987) 153
Segall, Z., *see* Gregoretti, F. 19 (1987) 85
Senadji, M., *see* Hou, K.M. 19 (1987) 401
Staszny, G., *see* Allo, G. 19 (1987) 19, 27

Tabak, D., The Intel 80386 and New 32-Bit Microprocessors 19 (1987) 59
Tempelmeier, Th., Performance Analysis of a Microprogrammed Real-Time Operating System with an Interrupt-and-Abort Discipline 19 (1987) 233
Tomek, I., HARD – A Multilevel Hardware Simulation Package for Teaching 19 (1987) 417
Tourouta, E., *see* Piuri, V. 19 (1987) 335
Tsai, S.R., Tseng, L.M. and Beer, M.D., A Microprocessor-Based Logical Machine System 19 (1987) 375
Tseng, L.M., *see* Tsai, S.R. 19 (1987) 375

Vernazza, T., *see* Giordano, A. 19 (1987) 5